

L Number	Hits	Search Text	DB	Time stamp
1	210	(717/116).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:03
2	114	(717/118).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:04
3	107	(717/126).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:04
4	224	(717/127).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:04
5	174	(717/128).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:04
6	53	(717/147).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:04
7	101	(717/148).CCLS.	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:05
8	0	((717/116).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
9	0	((717/118).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
10	0	((717/126).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
11	0	((717/127).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
12	0	((717/128).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
13	0	((717/147).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
14	0	((717/148).CCLS.) and mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:06
15	0	mapp\$4 adj bytecode and state adj transition and interpreter	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:07
16	3	mapp\$4 adj bytecode	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:08
17	11056	Java or JAVA	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/06/18 11:39

18	0	(Java or JAVA) and mapp\$4 adj bytecode and infinite adj state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:38
19	0	(Java or JAVA) and mapp\$4 adj bytecode and finite adj state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:38
20	0	(Java or JAVA) and mapp\$4 adj bytecode and state adj trans\$5	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:38
21	3	(Java or JAVA) and mapp\$4 adj bytecode	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:38
22	7530	(Java or JAVA) and byte code adj program	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:39
23	2813	((Java or JAVA) and byte code adj program) and interpret\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:42
24	6	((((Java or JAVA) and byte code adj program) and interpret\$4) and model adj check\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:46
26	0	(((((Java or JAVA) and byte code adj program) and interpret\$4) and model adj check\$4) and state) and conditin\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:46
28	3	(((((Java or JAVA) and byte code adj program) and interpret\$4) and model adj check\$4) and state) and condition\$4) and mapp\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:47
27	6	(((((Java or JAVA) and byte code adj program) and interpret\$4) and model adj check\$4) and state) and condition\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:49
25	6	(((((Java or JAVA) and byte code adj program) and interpret\$4) and model adj check\$4) and state	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 11:49

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6651186 B1	20031118	50	Remote incremental program verification using API definitions	714/38
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6421808 B1	20020716	50	Hardware design language for the design of integrated circuits	716/1
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6353896 B1	20020305	45	Method and apparatus for testing event driven software	714/38
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6289502 B1	20010911	102	Model-based software design and validation	717/104
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6170083 B1	20010102	13	Method for performing dynamic optimization of computer code	717/158
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5163016 A	19921110	60	Analytical development and verification of control-intensive systems	716/5

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1	717/135		Schwabe, Judith E.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	703/14		McGeer, Patrick C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	717/126; 717/143		Holzmann, Gerard Johan et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	717/114; 717/126		Garland, Stephen J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	717/118; 717/145; 717/153		Adl-Tabatabai, Ali-Reza	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6			Har'El, Zvi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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